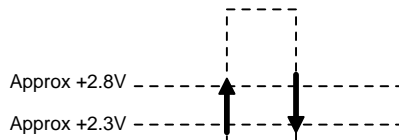


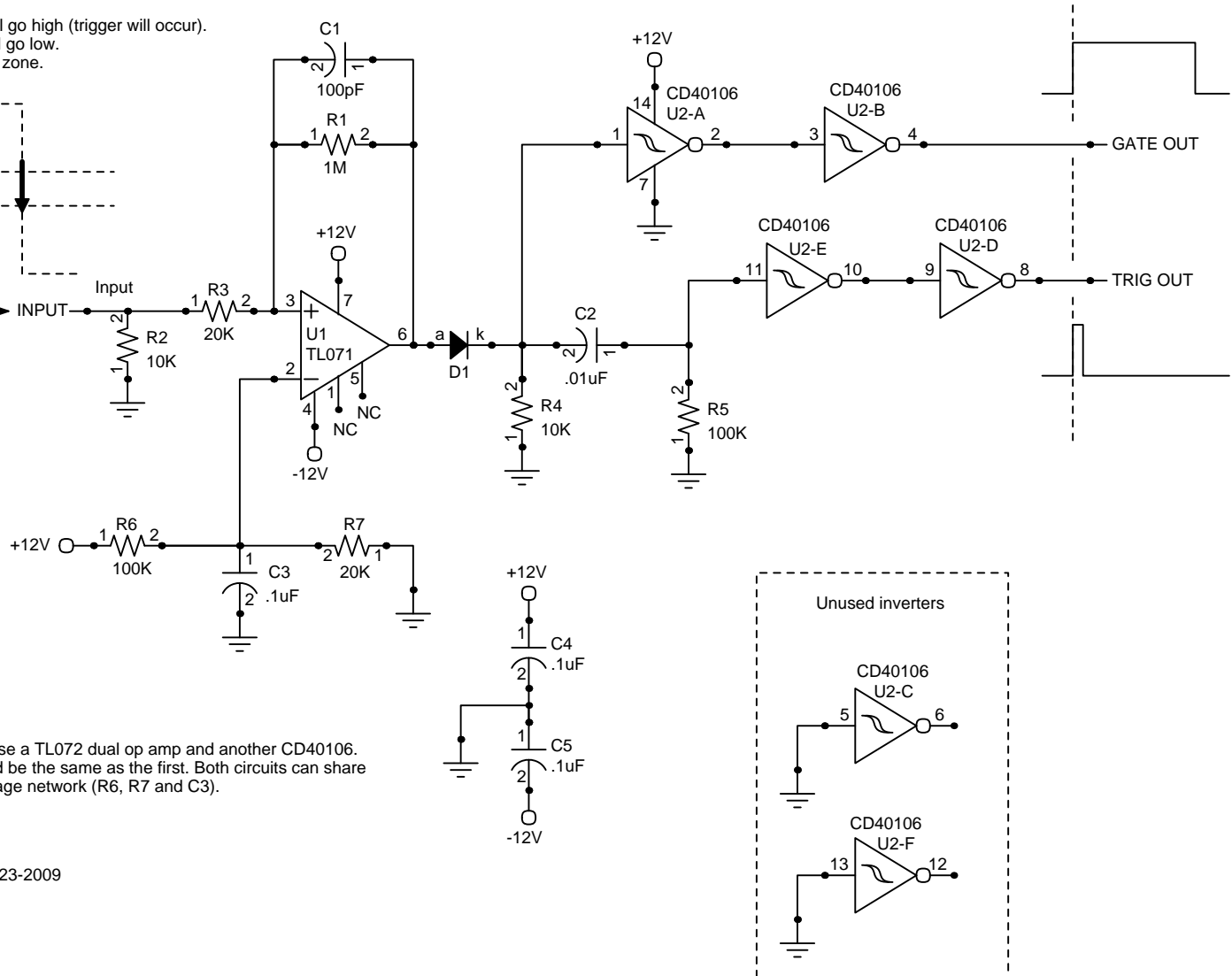
Gate Buffer With Comparator Input

This circuit will take a low voltage gate, clock or trigger and boost it to the positive supply voltage. It can be powered with +/-9 to +/-15 volts.

Above 2.8V the gate output will go high (trigger will occur).
Below 2.3V the gate output will go low.
Approximately 0.5V hysteresis zone.



From Clock or Gate with at least Ground to 3V output.



For a dual version just use a TL072 dual op amp and another CD40106. The second circuit would be the same as the first. Both circuits can share the same reference voltage network (R6, R7 and C3).

Drawn by Ray Wilson 9-23-2009