

RJWSOft Electronics

16 Step Analog Sequencer

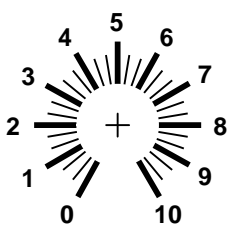
clock out




ext clk in




sequence rate



gate out




trigger out




sequence mode

reverse/reset step	action at step
10 11	reverse
5 16	reset


16 step sequence




use mode




random 16




reset




run/stop




back




forward




reset




run/stop



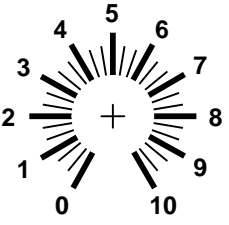
back





forward



portamento



voltage out



voltage out

