

Active Step

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16

⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕

Gate At Step Selection

On On On On On On On On On On On On On On On On

⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕

Off Off Off Off Off Off Off Off Off Off Off Off Off Off Off Off

Coarse Voltage Adjustment

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16

Fine Voltage Adjustment

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16

Step Duration Adjustment

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16

Step Duration

8 4 2 1

⊕ ⊕ ⊕ ⊕

Step duration equals (binary count +1) master clock cycles.

Master Clock Rate

⊕ Master

⊕ Q-Clock

Q-Clock Clock Source

Internal External

⊕ ⊕

Normal

Ext. Start ⊕

Clock Out ⊕

Ext. Clk In ⊕

Sequencer Mode

Action Step

Action At Step

Reverse ⊕

Reset ⊕

Stop ⊕

None (16 Step) ⊕

Use Action ⊕

Random 16 ⊕

Triggers Out

⊕ ⊕

Gates Out

⊕ ⊕

Multi-Gate/Trigger

⊕

Normal Gate/Trigger

Manual Control

Reset ⊕

Stop/Run ⊕

Back ⊕

Forward ⊕

Control Voltage Out

⊕ ⊕

Portamento

Control Voltage Out

⊕ ⊕