Operation With Consecutive Positive or Negative Slopes

0   1   2   3   4   5   6   7   0

Ready State
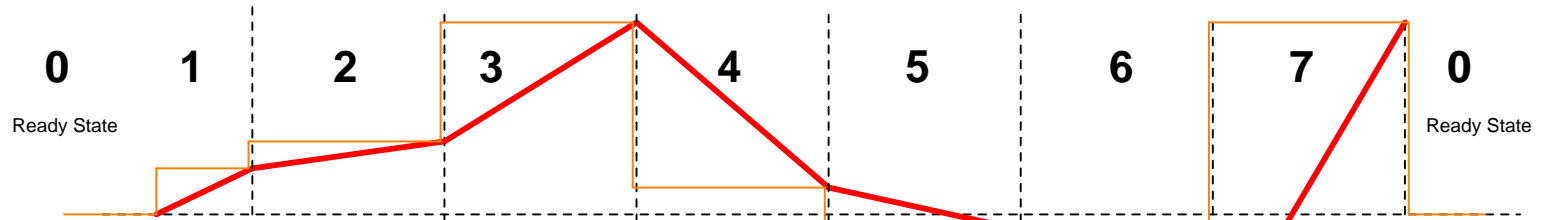
Ready State

Raw Ramp (RR)

Threshold Voltage (THV)

U3-B Pin 3   Gate pulse

U3-C Pin 6   Reset U6 to Ready State

U3-E Pin 10   Jam count "0001" into U6

U5-A Pin 1
U5-C Pin 6

U5-A Pin 2

U5-B Pin 4

Clock pulses masked by reset and count 0001 jam.

U5-B Pin 8

U6 Pin 5

Envelope Repeat (switch S3 closed)

ZIO

U5-F Pin 12

1

Clock to state 1 active when repeat switch is closed